

TIME TABLE 2021-22

ACHARYA NARENDRA DEV COLLEGE

(ODD SEMESTER)(Tentative)

DEPARTMENT OF ELECTRONICS

B.Sc (H) Electronic Science Semester V

PERIOD	1st	2nd	3rd	4th	5th	6th	7th	8th
DAY	8:45 a.m.– 9:45 a.m.	09:45 a.m.– 10:45a.m.	10:45a.m.– 11:45 a.m.	11:45 a.m.– 12:45p.m.	1:00p.m. – 2:00p.m.	2:00p.m.– 3:00p.m.	3:00p.m.– 4:00 p.m.	4:00p.m.– 5:00p.m.
MON	Electromagnetics Theory DK	DSE.2 Theory GG	Microprocessor Theory AG		Microprocessors/DSE1/ Electromagnetics Lab B1, B2,B3 (GG,VD,DK)		Microprocessors/DSE1 /Electromagnetics Lab B1, B2,B3 (AG,VD,DK)	
TUE	Electromagnetics Theory DK		Microprocessor Theory AG	DSE 1 Theory VD	DSE 2 Lab MB (S. No .1-21) GG (S. No . 22-42)			
WED	Electromagnetics Theory DK		DSE.2 Theory GG	DSE 1 Theory VD	Microprocessors/DSE1/Electromagnetics Lab B2, B3,B1 (AG,VD,DK)			
THU	Electromagnetics Theory DK	DSE.2 Theory GG	Microprocessor Theory AG		DSE 1 Theory VD	DSE 1 Theory VD		
FRI			Microprocessor Theory AG	DSE.2 Theory GG	Microprocessors/DSE1/Electromagnetics Lab B3, B1,B2 (AG,VD,DK)			
SAT								

AG- Dr. Amit Garg

VD- Mr. Vishal Dhingra

DK- Mr. Dinesh Kumar

MB- Dr. Monica Bhattacharya

GG- Ms. Gauri Ghai

B1,B2,B3- Practical Batches

DSE1-Power Electronics

DSE2-Semiconductor Fabrication & Characterization

Convenor

Principal

No Changes will be permitted without prior permission of the Principal